

Accelerating Simulations in gem5

A presentation by

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Outline of this session

- We will first address <u>why</u> do we need to make simulations faster, and, then we will see <u>how</u> can we make simulations faster.
- We will work together to create a *runscript,* that we will use throughout this session.
- We will iterate over each of the techniques of accelerating simulations, and work on several hands-on sessions.
- We will keep on modifying the *runscript*.

> cd materials/using-gem5/09-accelerating-simulations/



Hands-on Session: Matrix Multiply

kaustavg@citra:/scr/kaustavg/projects/bootcamp-code\$ g++ mm_base.cpp kaustavg@citra:/scr/kaustavg/projects/bootcamp-code\$./a.out Printing Statistics :: Wall Clock Time

Program: 4.7777e-05 s Matrix Multiply: 4.191e-06 s

kaustavg@citra:/scr/kaustavg/projects/bootcamp-code\$ g++ mm_base.cpp kaustavg@citra:/scr/kaustavg/projects/bootcamp-code\$./a.out Printing Statistics :: Wall Clock Time

Program: 0.00500679 s Matrix Multiply: 0.00389788 s

Program: 9.66581 s Matrix Multiply: 9.53323 s

kaustavg@citra:/scr/kaustavg/projects/bootcamp-code\$

> cd data-files/base

> g++ mm_base.cpp

- Go to the *data-files* directory.
- Compile the matrix multiply (mm_base.cpp) program on your host machine.
 What's wrong?100 matrix.
- Increase the size to 1000x1000.



How do we make gem5 simulations faster?





Creating the pre-requisite runscript

- We need to write a configuration script to execute the *mm* code.
- We will use the **SimpleBoard** in gem5 to do SE mode simulations.
- This will be a work along session.
- There are three **TODO**s marked in the runscript.
- Run the binary file with 100x100 elements.

> cd 00-prerequisite-runscript



Rationale Behind Accelerating Simulations

- We need a mechanism in gem5 for the simulated program can **communicate** with the host machine.
- This allows us to **manipulate** the runscript.
- We can keep only **essential statistics** of the simulation.



gem5 EXIT Events

- EXIT events in gem5 allows us to **communicate** from the simulator to the host.
- These are implemented as *magic instructions* or *memory mapped I/O*.
- There are different types of **EXIT Events** in gem5.



Different types of EXIT Events in gem5

- ExitEvent.**EXIT**
- ExitEvent.CHECKPOINT
- ExitEvent.FAIL
- ExitEvent.SWITCHCPU
- ExitEvent.WORKBEGIN
- ExitEvent.WORKEND
- ExitEvent.**USER_INTERRUPT**
- ExitEvent.**MAX_TICK**



Annotations

- We only care about **important regions** during the simulation.
- In other words, we have <u>regions of interests</u> or <u>ROIs</u>.
- How do we make gem5 <u>understand</u> ROIs?

The m5 utility!



The *m5* instruction

- What is <u>**m5**</u>?
- m5 can be implemented as a <u>magic instruction</u> or as <u>memory-mapped I/O</u> or <u>MMIO</u>.
- We add m5 library calls in the source code.



m5 Library Calls

- We need to **identify** ROIs.
- Look at the previous example, but with annotations.
- We use m5_work_begin(M, N) at the beginning and m5_work_end(M, N) at the end of the ROI.
- This resets the statistics which is recorded in the m5out/stats.txt file.

61	// Naive matrix multiplication code. It performs N^3 computations. We also
62	<pre>// keep a track of time for this part of the code.</pre>
63	
64	<pre>auto mm_start = std :: chrono :: high_resolution_clock :: now();</pre>
65	
66	// annotating the ROI
67	//
68	#ifdef GEM5
69	m5_work_begin(0, 0);
70	#endif
71	
72	for(int i = 0 ; i < N ; i++)
73	for(int $j = 0$; $j < N$; $j++$)
74	for(int $k = 0$; $k < N$; $k++$)
75	C[i][j] += A[i][k] * B[k][j];
76	
77	// end of ROI
78	11
79	#ifdef GEM5
80	m5_work_end(0);
81	#endif
82	
83	<pre>auto mm_end = std :: chrono :: high_resolution_clock :: now();</pre>
84	
85	<pre>// Free the memory allocated.</pre>
86	11
87	delete data_A;
88	delete data_B;
89	delete data_C;
90	delete A;
91	delete B;
92	delete C;
0.2	



Annotation Example

- Go to data_files/annotated
- Open and review the mm_annotated.cpp file.
- **Compile** the mm_annotated.cpp

Wait! How do we compile the code?



Recapping how to compile annotated code

- The m5 utility must be <u>compiled</u> in the util/m5.
- Change the **GEM5_HOME** variable.
- We need to <u>modify</u> the CFLAGS and LDFLAGS in the *Makefile*.
- We need to <u>include</u> the m5ops.h file from *include/gem5*/directory.
- · Make

> cd gem5

→ m5 git:(stable) × scons build/x86/out/m5 scons: Reading SConscript files ... Checking for java package org.junit...(cached) no junit test framework not found, not build java wrapper test Checking whether pkg-config program exists.../usr/bin/pkg-config Checking for pkg-config package lua51...(cached) no lua 5.1 not detected, not building lua wrapper. scons: done reading SConscript files. scons: Building targets ...

19	#CFLAGS
20	
21	CFLAGS=-std=c++14 -static -O3 -I\$(GEM5_HOME)/include
22	
23	#LDFLAGS
24	
25	LDFLAGS=-L\$(GEM5_HOME)/util/m5/build/\$(TARGET_ISA)/out -lm5
26	

9	
10	#define GEM5
11	
12	#ifdef GEM5
13	<pre>#include <gem5 m5ops.h=""></gem5></pre>
14	#endif
15	



Hands-on Session I: Modify the runscript

- Modify the original runscript to execute the *mm_annotated* binary.
- Print the total number of ticks.
- Print the EXIT Event.
- Look at the number of ticks in the m5out/stats.txt file.

```
# The `simulator` module has these methods which can be helpful for this
# hands-on session:
#
# simulator.get_current_tick() -> returns the current tick
# simulator.get_last_exit_event_cause() -> returns the last EXIT event cause
```

> cd 01-modifying-runscript



Some m5 function calls in gem5

- m5_reset(M, N): It resets the stats file. A new section is generated in the m5out/stats.txt file.
- m5_dump_reset_stats(M, N): It dumps the stats and resets the stats file.
- m5_work_begin(X, Y): It starts keeping stats.
- m5_work_end(X, Y): It stops keeping stats.
- m5_exit(M): It drops a ExitEvent.EXIT EXIT Event in the runscript.
 - > cd gem5/include/gem5
 - > code m5ops.h



Understanding the different m5 function calls

-			
2	Begin Simul	ation Statistics	
3	simSeconds	0.173081	<pre># Number of seconds simulated (Second)</pre>
4	simTicks	173081275137	<pre># Number of ticks simulated (Tick)</pre>
5	finalTick	173081275137	# Number of ticks from beginning of simulation (restored from checkpoints and never reset) (Tick)
6	simFreq	10000000000	<pre># The number of ticks per simulated second ((Tick/Second))</pre>
7	hostSeconds	8.03	<pre># Real time elapsed on the host (Second)</pre>
8	hostTickRate	21564253748	<pre># The number of ticks simulated per host second (ticks/s) ((Tick/Second))</pre>
9	hostMemory	1225464	# Number of bytes of host memory used (Byte)
10	simInsts	2386715	<pre># Number of instructions simulated (Count)</pre>
11	simOps	4150319	# Number of ops (including micro ops) simulated (Count)
12	hostInstRate	297357	<pre># Simulator instruction rate (inst/s) ((Count/Second))</pre>
13	host0pRate	517082	<pre># Simulator op (including micro ops) rate (op/s) ((Count/Second))</pre>

Fig. Executing without ROI annotations.

1

1			
2	Begin Simula	tion Statistics	
3	simSeconds	0.008159	# Number of seconds simulated (Second)
4	simTicks	8159148018	# Number of ticks simulated (Tick)
5	finalTick	8159148018	# Number of ticks from beginning of simulation (restored from checkpoints and never reset) (Tick)
6	simFreq	100000000000	# The number of ticks per simulated second ((Tick/Second))
7	hostSeconds	0.41	# Real time elapsed on the host (Second)
8	hostTickRate	19712732539	<pre># The number of ticks simulated per host second (ticks/s) ((Tick/Second))</pre>
9	hostMemory	1180944	# Number of bytes of host memory used (Byte)
10	simInsts	111968	# Number of instructions simulated (Count)
11	simOps	209867	<pre># Number of ops (including micro ops) simulated (Count)</pre>
12	hostInstRate	270466	<pre># Simulator instruction rate (inst/s) ((Count/Second))</pre>
13	host0pRate	506938	<pre># Simulator op (including micro ops) rate (op/s) ((Count/Second))</pre>

Fig. Executing with ROI begin and end.



Understanding the different m5 function calls

- 2 ----- Begin Simulation Statistics -----
- 3 simSeconds
- 4 simTicks

finalTick

0.000956 955552491 8161572591

- # Number of seconds simulated (Second)
- # Number of ticks simulated (Tick)
- # Number of ticks from beginning of simulation (restored from checkpoints and never reset) (Tick)

Fig. Executing with m5 reset stats(0, 0).

1 2 3 4 5	Begin Simulation Statistics simSeconds simTicks (finalTick (0.006500 # 6499623204 # 6499623204 #	Number of seconds simulated (Second) Number of ticks simulated (Tick) Number of ticks from beginning of simulation (restored from checkpoints and never reset) (Tick)
465 466 467 468 469 470	End Simulation Statistics simSeconds simTicks finalTick 7	0.000706 # 706311315 # 205934519 #	Number of seconds simulated (Second) Number of ticks simulated (Tick) Number of ticks from beginning of simulation (restored from checkpoints and never reset) (Tick)
918 919 920 921 922 923 924	End Simulation Statistics simSeconds simTicks finalTick 8	0.000955 # 955384326 # 8161318845 #	Number of seconds simulated (Second) Number of ticks simulated (Tick) Number of ticks from beginning of simulation (restored from checkpoints and never reset) (Tick)

Fig. Executing with *m5_dump_reset_stats(0, 0)*.



Annotating a real-world workload.

- As computer architects, we must work with standard benchmark programs to <u>analyze the performance and requirements</u> of the proposed design.
- It is important to understand the **characteristics** of the workload.
- In this session, we will investigate the <u>LLVM's Stanford Benchmark Suite</u>
 [1].

[1] C. Lattner and V. Adve, "LLVM: a compilation framework for lifelong program analysis & transformation," International Symposium on Code Generation and Optimization, 2004. CGO 2004., 2004, pp. 75-86, doi: 10.1109/CGO.2004.1281665.



Hands-on Session II: Annotating real-world workload.

- We will annotate *Bubblesort* in this session.
- Download the benchmarks.
- Edit the Bubblesort.cpp file.
- Compile
- Simulate
- > cd 02-annotating-llvmstanford

140	
140	weid Dubble(int run) (
141	vola Bubble(int run) {
142	int 1,];
143	bInitarr();
144	top=srtelements;
145	
146	while (top>1) {
147	
148	i=1;
149	while (i <top)="" td="" {<=""></top>
150	
151	<pre>if (sortlist[i] > sortlist[i+1]) {</pre>
152	<pre>j = sortlist[i];</pre>
153	<pre>sortlist[i] = sortlist[i+1];</pre>
154	<pre>sortlist[i+1] = j;</pre>
155	}
156	i=i+1;
157	3
158	
159	<pre>top=top-1;</pre>
160	}
161	<pre>if ((sortlist[1] != littlest) (sortlist[srtelements] != biggest))</pre>
162	<pre>printf ("Error3 in Bubble.\n");</pre>
163	<pre>printf("\n%d\n", sortlist[run + 1]);</pre>
164	
165	}
166	
167	int main()
168	{
169	int i:
170	for (i = 0: i < 10: i++) Bubble(i):
171	return 0:
172	}
172	-



Analyzing Hands-on Session II

- Are there repeating patterns? The entire *Bubble(i)* function is being called for 10 times.
- Do we need the same stats 10 times?
- Let us use m5_exit(M) at the end.

> cd 02-annotating-llvm-

stanford/optimized

147	void Rubble(int run) {
1/12	int i i
140	hTaitann():
149	ton-spielements:
150	#ifdof GEME
151	mE work bogin(A_A):
152	tondif
155	while (top>1) [
154	while (copyr) {
155	i_1.
150	ubile (itten) (
157	while (ixtop) {
150	if (contlict[i] > contlict[i,1]) (
160	i = contlict[i]
161	j = sortlist[i],
162	<pre>sortlist[i] = sortlist[i+i], contlist[i:1] = i;</pre>
162	SUPERISC[ITT] = J,
164	
165	
165	I
167	ton-ton-1:
169	ι τομ-τομ-1,
169	j if ((sontlist[1] != littlest) (sontlist[sntelements] != higgest))
170	printf ("Error3 in Bubble.\n"):
171	$printf("\n%d\n", sortlist[run + 1]);$
172	#ifdef GEM5
173	m5 work end(0, 0):
174	#endif
175	
176	}
177	
178	<pre>int main()</pre>
179	{
180	int i;
181	<pre>for (i = 0; i < 10; i++) Bubble(i);</pre>
182	return 0;
183	}
184	



More on workload annotation

 Refer to previously annotated benchmark programs such as NAS-Parallel-Benchmarks [2], parsec [3] and gabps [4] for understanding more on workload annotation.

[2] Bailey, David H., et al. "The NAS parallel benchmarks summary and preliminary results." Supercomputing'91: Proceedings of the 1991 ACM/IEEE conference on Supercomputing. IEEE, 1991.

[3] Christian Bienia *et al.* 2008. The PARSEC benchmark suite: characterization and architectural implications. In Proceedings of the 17th international conference on Parallel architectures and compilation techniques (PACT '08). ACM, New York, NY, USA, 72-81. https://doi.org/10.1145/1454115.1454128

[4] S. Beamer, K. Asanovi ć, and D. Patterson, "The gap benchmark suite," 2015. [Online]. Available: https://arxiv.org/abs/1508.03619



> echo "Restoring the session: Accelerating Simulations"





Housekeeping

- There was a mistake in
 the m5_work_begin()
 and m5_work_end() slides/codes.
- Thanks for pointing this out.
- The set_se_workload never
 exits when encounters a work
 item.
- We must explicitly set `board.exit_on_work_items = True` (or wait for the next patch).

154 155	<pre># Set whether to exit on work items for the se_workload board.exit_on_work_items = True</pre>	
build/X80 build/X80 Printing	86/sim/simulate.cc:194: info: Entering event queue @ 1225239867. Starting simulation 86/sim/simulate.cc:194: info: Entering event queue @ 7273090629. Starting simulation g Statistics :: Wall Clock Time	
build/204	26/cim/mom_state_sc:442; infe; Increasing stack size by one name	
Program.	00/SIM/Mem_State.tt.445. INTO. Increasing Statk Size by one page.	
Matrix Mu	Multiply: 0.00604789 5	

Exiting @ tick 7285127580 because exiting with last active thread context. → gem5 git:(stable) X

1			
2	Begin Simulation Statis	tics	
3	simSeconds	0.006048	<pre># Number of seconds simulated (Second)</pre>
4	simTicks	6047850762	# Number of ticks simulated (Tick)
5	finalTick	7273087632	# Number of ticks from beginning of simulation (restored from checkpoint
6	simFreq	100000000000	<pre># The number of ticks per simulated second ((Tick/Second))</pre>
7	hostSeconds	5.53	# Real time elapsed on the host (Second)
8	hostTickRate	1093265275	# The number of ticks simulated per host second (ticks/s) ((Tick/Second)
9	hostMemory	1185672	# Number of bytes of host memory used (Byte)
700	system.workload.inst.arm	0	# number of arm instructions executed (Count)
701	system.workload.inst.quiesce	0	<pre># number of quiesce instructions executed (Count)</pre>
702			
703	End Simulation Statisti	cs	
704			
705	Begin Simulation Statis	tics	
706	simSeconds	0.000012	<pre># Number of seconds simulated (Second)</pre>
707	simTicks	11962359	# Number of ticks simulated (Tick)
708	finalTick	7285049991	# Number of ticks from beginning of simulation (restored from checkpoint:
709	simFreq	100000000000	<pre># The number of ticks per simulated second ((Tick/Second))</pre>
710	1.16.1	• ••	

66 from gem5.simulate.exit_event_generators import_default_exit_generator

158
159 simulator = Simulator(board=board, on_exit_event = {
160 ExitEvent.WORKEND : default_exit_generator()
161 }
162)



Checkpoints in gem5

- Simulations take a **long time**.
- We need to be able to <u>save</u> and <u>restore</u> the state of simulation in gem5.
- · Checkpoints were originally designed to be used with **<u>AtomicSimpleCPU.</u>**
- We can take checkpoints **whenever** we want.
- We will use the EXIT Event: **ExitEvent.CHECKPOINT**.
- It can be triggered by the function m5_checkpoint(M, N).



Checkpoints in gem5

- The *cpt* file stores the simulation values of all the SimObjects.
- What happens when we want to switch components?
- What happens to the cache when you load a checkpoint?

```
## checkpoint generated: Thu Dec 30 09:24:09 2021
2
    [board.processor.cores.core]
    instCnt=1
     pid=4294967295
    [board.processor.cores.core.xc.0]
    pendingSmi=false
10
    smiVector=0
    pendingNmi=false
11
12
   nmiVector=0
    pendingExtInt=false
13
14
   extIntVector=0
15
    pendingInit=false
   initVector=0
16
17
    pendingStartup=false
18
    startupVector=0
    startedUp=false
19
    pendingUnmaskableInt=false
20
    pendingIPIs=0
21
    IRRV=0
22
    ISRV=0
23
    apicTimerEventScheduled=false
24
25
    apicTimerEventTick=0
    _status=0
26
                                  27
    floatRegs.i=0
    vecRegs=
28
29
    vecPredRegs=
    30
    ccRegs=0 0 0 0 0
31
32
    pc=4214660
33
    _upc=0
34
    npc=4214668
35
    nupc=1
36
     size=0
37
38
    [board.processor.cores.core.workload]
39
    brkPoint=6135808
    stackBase=140737488351232
40
    stackSize=4096
41
42
    maxStackSize=8388608
43
    stackMin=140737488347136
    nextThreadStackBase=140737479962624
44
45
    mmapEnd=140737354133504
46
47
    [board.processor.cores.core.workload.vmalist]
48
    size=2
```



Checkpointing the mm code

- Let us checkpoint the simulation before the matrix multiplication starts.
- We need to add an
 m5_checkpoint(0, 0)
- The *simulator* module can easily restore the saved checkpoint.
- We also need to modify the runscript.

٠	72			
	73 \sim	#ifdef GEM5		
	74	<pre>m5_checkpoint(0, 0);</pre>		
	75	#endif		
	76			
	77	for(int i = 0 ; i < N ; i++)		
	78	for(int $j = 0$; $j < N$; $j++$)		
	79	for(int $k = 0$; $k < N$; $k++$)		
	80	<pre>C[i][j] += A[i][k] * B[k][j];</pre>		
	81			
	82 🗸	// end of ROI		
	83	11		

115	# restoring Previously Stored checkpoint.
116	
117	from pathlib import Path
118	<pre>simulator = Simulator(board=board, checkpoint_path=Path("checkpoint-dir"))</pre>
119	

123			
124	# Creating the checkpoint directory.		
125			
126	<pre>if not os.path.exists(os.path.join(os.getcwd(), "checkpoint-di</pre>	r")):	
127	<pre>os.makedirs(os.path.join(os.getcwd(), "checkpoint-dir"))</pre>		
128			
129	# Saving the checkpoint.		
130			
131	<pre>simulator.save_checkpoint("checkpoint-dir")</pre>		



More on Checkpoints

- Checkpoints can be taken after a certain number of ticks have been simulated.
- Checkpoints can also be taken periodically.
- On can also trigger
 ExitEvent.USER_INTERRUPT and
 write a checkpoint to continue the simulation later.

112		
113	simulator = Simulator(board=board)	
114		
115	<pre>simulator.run(max_ticks = 10000)</pre>	
116		
117 \sim	print(
118 $\scriptstyle{\sim}$	<pre>"Exiting @ tick {} because {}.".format(</pre>	
119	<pre>simulator.get_current_tick(),</pre>	
120	<pre>simulator.get_last_exit_event_cause(),</pre>	
121)	
122)	
123		
124	<pre>simulator.save_checkpoint("checkpoint-dir")</pre>	
125		

or i in range(10):	
<pre>simulator.run(max_ticks = 1000000)</pre>	
print(
<pre>"Exiting @ tick {} because {}.".format(</pre>	
<pre>simulator.get_current_tick(),</pre>	
<pre>simulator.get_last_exit_event_cause(),</pre>	
)	
)	
<pre>simulator.run("checkpoint-dir")</pre>	
	<pre>print("Exiting @ tick {} because {}.".format(simulator.get_current_tick(), simulator.get_last_exit_event_cause(),) simulator.run("checkpoint-dir")</pre>



Hands-on Session III: Restoring a checkpoint

- In this assignment, you will be provided with a **cpt** file of the mm code.
- This checkpoint file was taken after executing 1T ticks on the machine defined by the runscript.
- Your objective will be to:
 - Restore the checkpoint.
 - Execute the next 10B ticks.
 - Save the checkpoint.

> cd 03-checkpoints

115 116	# restoring Previously Stored checkpoint.
117	from pathlib import Path
118	<pre>simulator = Simulator(board=board, checkpoint_path=Path("checkpoint-dir"))</pre>
119	
178	for i in range(10):
179	<pre>simulator.run(max_ticks = 1000000)</pre>
180	
181	print(
182	<pre>"Exiting @ tick {} because {}.".format(</pre>
183	<pre>simulator.get_current_tick(),</pre>
184	<pre>simulator.get_last_exit_event_cause(),</pre>
185	
186)
187	
188	<pre>simulator.run("checkpoint-dir")</pre>
189	



Fast-forwarding

- Once we have our <u>code annotated</u>, we <u>fastforward</u> our simulations.
- gem5 supports switchable CPUs.
- We use kvm or atomic cpu to simulate the non-essential regions of the code. Then we switch to any timing cpu.





Sampling

- We can sample *essential* parts of the simulation to find a *representative statistics* for the whole workload.
- We use a combination of both *kvm/atomic* and *timing* CPUs.



[*] Erez Perelman, Greg Hamerly, Michael Van Biesbrouck, Timothy Sherwood, and Brad Calder. 2003. Using SimPoint for accurate and efficient simulation. SIGMETRICS Perform. Eval. Rev. 31, 1 (June 2003), 318-319. https://doi.org/10.1145/885651.781076





Thank You!



