



Welcome to the **gem5 bootcamp** 2022

A presentation by
The Davis Computer Architecture
Research Group

The Team

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About you all!

At least 25 different universities

Some starting next year, most in their first year, a handful in 2nd and 3rd years

Learn more at lunch and other activities!



How the bootcamp will work

9am – 12pm Morning session

12pm – 1pm Lunch

1pm – 4pm Afternoon session

Mix of lectures and coding exercises

Using github codespaces for coding

Sign up at <https://classroom.github.com/a/hM0bZ4xY>

*Make sure to sign in to education.github.com

Find the slides here: <https://tinyurl.com/gem5bootcamp-slides>

Discussions on Slack: <https://tinyurl.com/gem5slack>



Getting started with Codespaces

<https://classroom.github.com/a/hM0bZ4xY>

The screenshot shows a GitHub repository page for 'gem5bootcamp/bootcamp-assignment-jlowepower'. The 'Code' button is circled in red. Below it, the 'Codespaces' tab is also circled in red. The main content area displays a 'Welcome to cloud editing' message with a 'Create codespace on main' button, which is also circled in red. The repository's commit history is visible on the right side of the page.

Commit	Author	Message	Time
9aab8be	github-classroom[bot]	Add online IDE url	now
		2 commits	
	evcontainer	Initial commit	now
	scode	Initial commit	now
	lata	Initial commit	now
	ncludes	Initial commit	now
	N	Initial commit	now
	ayouts	Initial commit	now
	ass	Initial commit	now
	Psets	Initial commit	now
	Nocker	Initial commit	now
	Pi	Initial commit	now
	am5 @ a100731	Initial commit	now

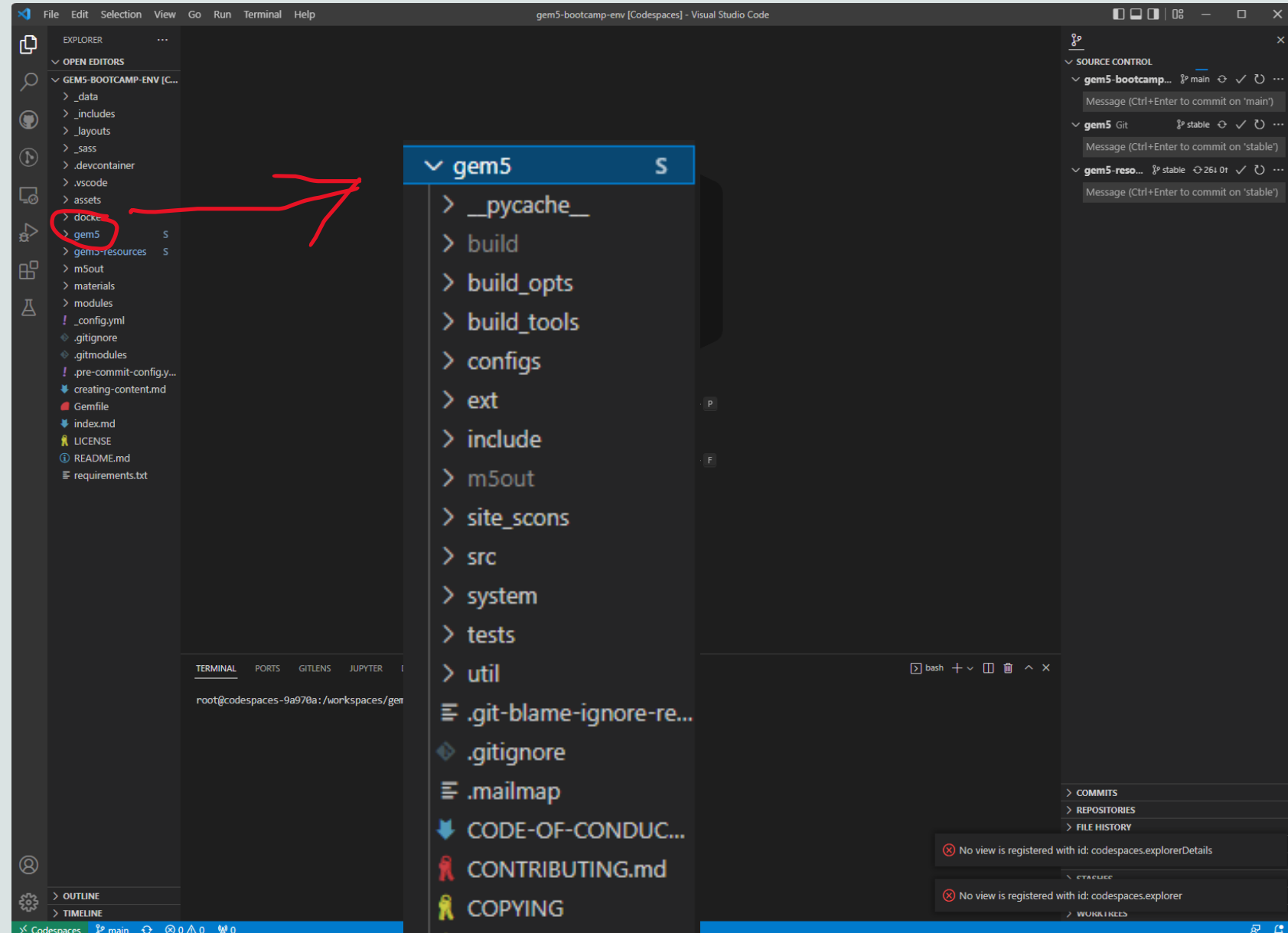
Codespaces!

16 core VM
Use -j17

gem5 code, resources

Materials and reference
for this bootcamp

Much more!



Plan for the week

Monday

Introduction

- Getting started with gem5: using, develop, and simulation

Using gem5

- gem5 standard library

Tuesday

Using gem5

- General using
- gem5 models: caches, CPUs, memory

- Full system sim
- Accelerating simulation

Wednesday

gem5 devel

- First SimObject, params, events, memory ops

- Instruction execution
- Adding an instruction

Thursday

gem5 devel

- Classic caches
- Ruby and SLICC
- OCN and Garnet

- gem5's GPGPU model

Friday

Extra topics

- Contributing to gem5

- Using other simulators w/ gem5

- **Whatever you want!**



My goals

Make gem5 less painful than it was for me

Give you a vocabulary for asking questions

Provide a reference for the future

Give you material to take back and teach your colleagues



Other admin things

Food is covered only at dining hall, Segundo (except travel and reception)

Reimbursement information on Friday (will include in email as well)



More resources

Bootcamp website: <https://gem5bootcamp.github.io/gem5-bootcamp-env/>

Classroom: <https://classroom.github.com/a/hM0bZ4xY>

Slack: <https://tinyurl.com/gem5slack>

Bootcamp source: <https://github.com/gem5bootcamp/gem5-bootcamp-env>

Code: <https://gem5.googlesource.com/>

gem5: <https://www.gem5.org/>

Code review: <https://gem5-review.googlesource.com/>

YouTube: https://www.youtube.com/channel/UCCpCGEj_835WYmbB0g96lZw





Created at Michigan by Steve Reinhardt and his students, principally Nate Binkert.

“A tool for simulating systems”



Two Views of M5

1. A framework for event-driven simulation
 - Events, objects, statistics, configuration
 2. A collection of predefined object models
 - CPUs, caches, busses, devices, etc.
-
- This tutorial focuses on #2
 - You may find #1 useful even if #2 is not





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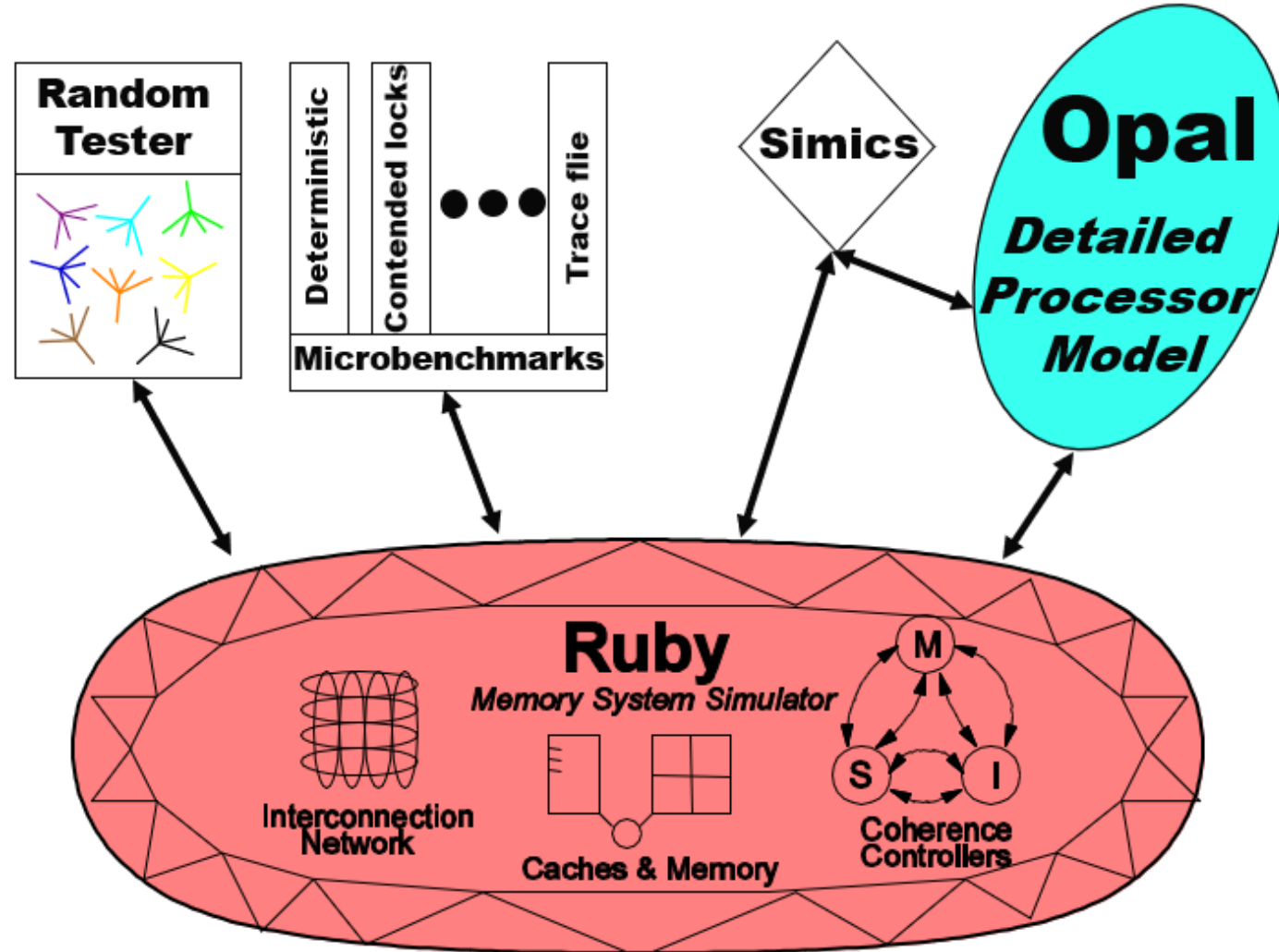


Created at Wisconsin by students of Mark Hill and David Wood.

Detailed memory system



GEMS From 50,000 Feet





Created at Michigan by students of Steve Reinhardt, principally Nate Binkert.

“A tool for simulating systems”



Created at Wisconsin by students of Mark Hill and David Wood.

Detailed memory system



What is gem5?

Michigan m5 + Wisconsin GEMS = gem5

“The gem5 simulator is a modular platform for computer-system architecture research, encompassing system-level architecture as well as processor microarchitecture.”

Lowe-Power et al. **The gem5 Simulator: Version 20.0+**. ArXiv Preprint ArXiv:2007.03152, 2021.
<https://doi.org/10.48550/arXiv.2007.03152>

Nathan Binkert, Bradford Beckmann, Gabriel Black, Steven K. Reinhardt, Ali Saidi, Arkaprava Basu, Joel Hestness, Derek R. Hower, Tushar Krishna, Somayeh Sardashti, Rathijit Sen, Korey Sewell, Muhammad Shoaib, Nilay Vaish, Mark D. Hill, and David A. Wood. 2011. **The gem5 simulator**. *SIGARCH Comput. Archit. News* 39, 2 (August 2011), 1-7.
DOI=<http://dx.doi.org/10.1145/2024716.2024718>

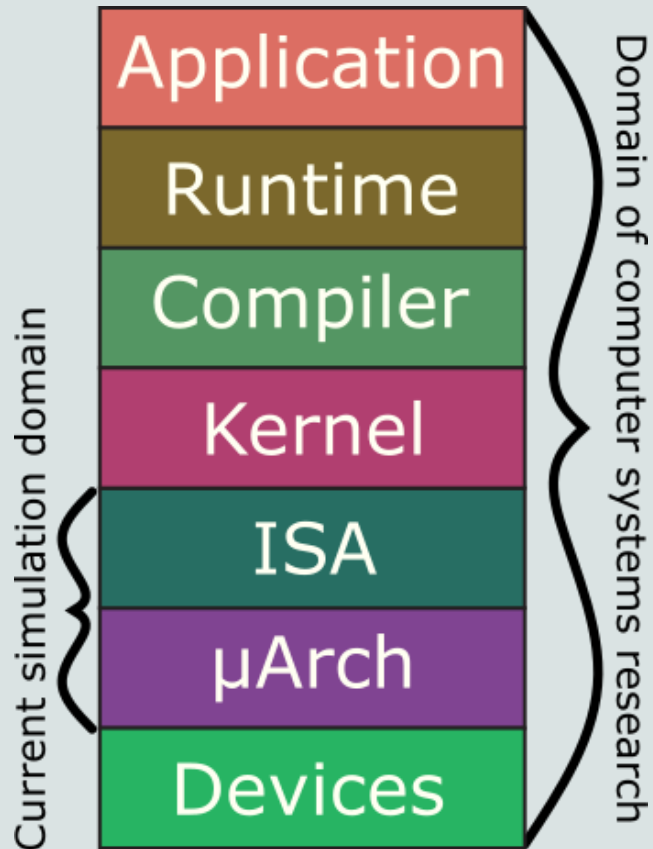




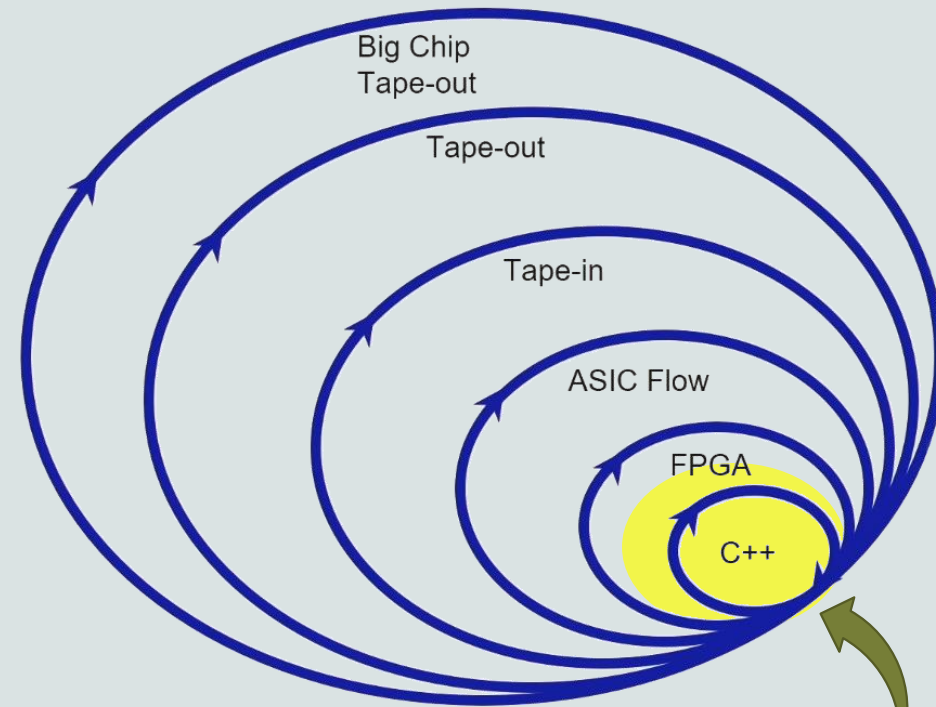
gem5 Version 20.0

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gem5's goals



Agile Hardware Dev. Methodology



From Hennessey and Patterson
Turing Lecture

gem5's goals

Anyone (including non-architect) can download and use gem5

Used for cross-stack research:

Change kernel, change runtime, change hardware, all in concert

Run full ML stacks, full AR/VR stacks... other emerging apps

We're close... just a lot of rough edges! ***You can help!***



The gem5 community

100s of contributors & 1000s(?) of users

Aim to meet the needs of

Academic research (most of you all!)

Industry research and development

Classroom use

Code of conduct (see repo)

I want to see the community grow through more events!



What is gem5 useful for?

mod of guest OS protocol
simulate new device
test new proc. arch
test new ISAs
New NoC topologies
Model cache coherence
New consistency protocol

Design space exploration

System-level studies

5-10 year-out ideas

Full-system simulation

Flexible simulation methodologies

Detailed cache coherence designs

Multi-ISA studies

What is gem5 **not** useful for?

tiny arch tweak
circuit-level design
physical properties \rightarrow power/area
fast simulation

Cycle accurate simulation
Low-level microarch. details
Circuit simulation
Detailed power analysis
Fast simulation
Functional emulation only

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Today

Morning: Introductions

- Introduction to the bootcamp
- Computer architecture simulation
- Intro to using gem5
- Intro to developing gem5

Afternoon: Using gem5

- gem5's standard library

